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Photo DMOS - FET Relay I PMA930

RELAY / ISO9001 / IATF16949 CERTIFIED

- Features
 - No moving parts
 - High reliability
 - Arc-Free with no snubbing circuits
 - 3750Vrms Input / Output isolation
 - Low driver power requirements (TTL/CMOS Compatible)
 - SMD package 6 Pin type in miniature design (6.4 x 8.8 x 3.4 mm)



Description

- The PMA930 is a 1-Form A solid state relay in a 6 pin SMD package that employs optically coupled MOSFET technology to provide 3750V of input to output isolation. The optically coupled input is controlled by a highly efficient GaAlAs infrared LED and MOS FETs on the output side.
- Application
 - Telecommunications (PC, Electronic notepad) / Measuring and Testing Equipment / Industrial Control / Security Equipments / High Speed Inspection Machine, etc.

Absolute Maximum Ratings (Ambient Temperature: 25°C)

	Item		Value	Units	Note	
Input	Continuous LED Current	I _F	50	mA		
	Peak LED Current	I _{FP}	1000	mA	f=100Hz, duty=1%	
	LED Reverse Voltage	V _R	5	V		
	Input Power Dissipation	P _{In}	75	mW		
Output	Load Voltage	VL	700	V (AC peak or DC)		
			600	mA	А	AC
	Load Current	ΙL	700	mA	В	DC
			800	mA	С	DC

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Absolute Maximum Ratings (Ambient Temperature: 25°C)

Item		Symbol	Value	Units	Note		
Output	Peak Load Current	I _{Peak}	1200	mA	100ms (1 pulse)		
	Output Power Dissipation	P _{out}	600	mW			
Total Power Dissipation		Ρ _T	650	mW			
I/O Breakdown Voltage		V _{I/O}	3750	Vrms	RH=60%, 1min		
Operating Temperature		T _{Opr}	-40 to +85	°C			
Storage Temperature		T _{Stg}	-40 to +100	°C			
Pin Soldering Temperature		T _{Sol}	260	°C	10 sec max.		

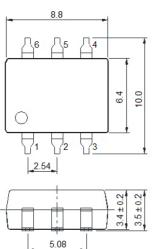
Electrical Specifications (Ambient Temperature: 25°C)

Item		Symbol	MIN.	TYP.	MAX.	Units	Note
Input	LED Forward Voltage	V_{F}		1.2	1.5	V	I _F =10mA
	Operation LED Current	I_{FOn}		0.8	5.0	mA	
	Recovery LED Current	I _{F Off}	0.35	0.8		mA	
	Recovery LED Voltage	V_{FOff}	0.7			V	
Output	On-Resistance	R _{On}		1.0	1.3	Ω	I _F =10mA, I _L =Rating, Time to flow is within 1 sec.
	Off-State Leakage Current	I _{Leak}			1.0	uA	V _L =Rating
	Output Capacitance	C _{Out}		1900		pF	V _L =0, f=1MHz
Transmission	Turn-On Time	T_{On}		0.5	2.0	ms	I _F =10mA,
	Turn-Off Time	T _{Off}		0.03	0.2	ms	I _L =Rating
Coupled	I/O Isolation Resistance	R _{I/O}	10 ¹⁰			Ω	DC500V
	I/O Capacitance	C _{I/O}		0.8	1.5	pF	f=1MHz

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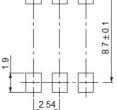
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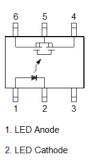
Dimensions (UNIT: mm)
Outline Dimensions





Recommended Mounting Pad





Wiring

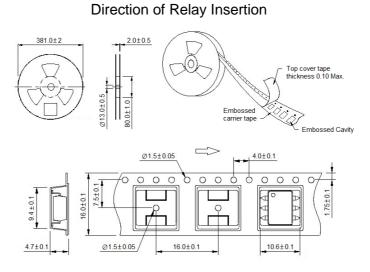
Diagram

4. Drain (MOS FET)

5. Source (MOS FET)

6. Drain (MOS FET)

Tape Packing

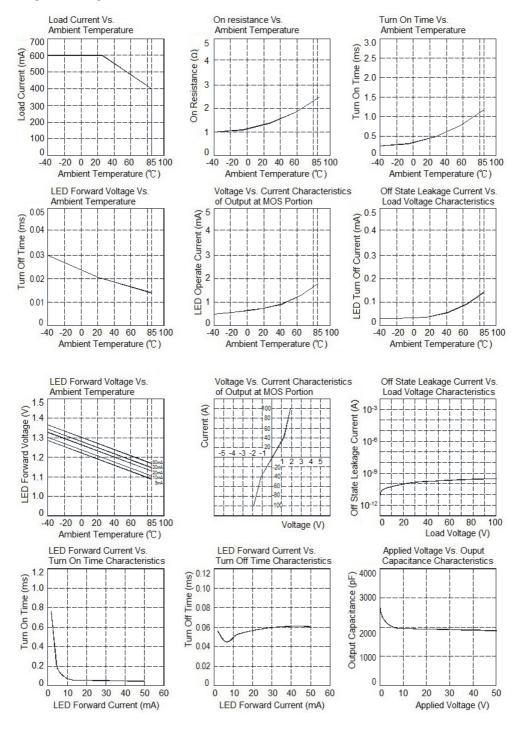


1,000pcs per reel

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• Engineering Data



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- Note: 1. There shall be leader of 230 mm minimum which may consist of carrier and or cover tape follower by a minimum of 160 mm of carrier tape sealed with cover tape.
 - 2. There shall be a minimum of 160 mm of empty component pockets sealed with cover tape.
 - 3. Devices are pockets in accordance with EIA standard EIA-481-A and specifications given above.
- Remark: 1) In case of no tolerance shown in outline dimension: outline dimension ≤1mm, tolerance should be ±0.2mm; outline dimension >1mm and ≤5mm, tolerance should be ±0.3mm; outline dimension >5mm, tolerance should be ±0.5mm.
 - 2) The tolerance without indicating for PCB layout is always ± 0.1 mm.

Disclaimer

The specification is for reference only. See to"Terminology and Guidelines" for more information. Specifications subject to change without notice. We could not evaluate all the performance and all the parameters for every possible application. Thus the user should be in a right position to choose the suitable product for their own application. If there is any query, please contact IOEC for the technical service. However, it is the user's responsibility to determine which product should be used only.

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